

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (original): A wiring board obtained by coating a copper paste on a ceramic green sheet and firing it to form a conductor layer and an insulating layer, the copper paste comprising a copper powder, an organic vehicle and at least one selected from the group consisting of: an  $\text{SiO}_2$  particle having an average particle size of 50 nm or less; and a ceramic particle having an average particle size of 100 nm or less and non-vitrifiable after sintering.
2. (original): A wiring board obtained by coating a copper paste on a ceramic green sheet and firing it to form a conductor layer and an insulating layer, the copper paste comprising a copper powder, an organic vehicle and an  $\text{SiO}_2$  particle having an average particle size of 50 nm or less.
3. (original): A wiring board obtained by coating a copper paste on a ceramic green sheet and firing it to form a conductor layer and an insulating layer, the copper paste comprising a copper powder, an organic vehicle and a ceramic particle having an average particle size of 100 nm or less and non-vitrifiable after sintering.
4. (original): The wiring board according to claim 1, wherein the conductor layer has a resistivity of  $3 \times 10^{-6} \Omega \cdot \text{cm}$  or less.
5. (original): The wiring board according to claim 1, wherein the insulating layer comprises an alkali metal in amount of 0.5 mol% or less in terms of oxide.

6. (original): The wiring board according to claim 1, wherein the conductor layer comprises an inorganic material having an average particle size of  $2 \mu\text{m}$  or less, the inorganic material being dispersed within the conductor layer so as not to be exposed to an outside of the conductor layer.

7. (original): The wiring board according to claim 1, wherein a surface of the conductor layer is subjected to a plating treatment.

8. (original): A wiring board comprising a conductor layer containing an inorganic material dispersed within the conductor layer, wherein in a cross section in a thickness direction of the conductor layer, a total area of the inorganic material having a particle size of  $2 \mu\text{m}$  or more is 5% or less of the sectional area of the conductor layer.

9. (original): A wiring board comprising a conductor layer containing an inorganic material dispersed within the conductor layer, wherein in a cross section in a thickness direction of the conductor layer, a total area of the inorganic material having a particle size of  $3 \mu\text{m}$  or more is 2% or less of the sectional area of the conductor layer.

10. (original): The wiring board according to claim 8, wherein a surface of the conductor layer is subjected to a plating treatment.

11. (canceled).

12. (canceled).

13. (canceled).

14. (canceled).

15. (currently amended): A method for producing a wiring board obtained by coating a copper paste on a ceramic green sheet and firing it to form a conductor layer and an insulating layer, the copper paste comprising a copper powder, an organic vehicle and at least one selected from the group consisting of: an SiO<sub>2</sub> particle having an average particle size of 50 nm or less; and a ceramic particle having an average particle size of 100 nm or less and non-vitrifiable after sintering, said method comprising the steps of:

coating the copper paste ~~according to claim 11~~ on a ceramic green sheet;

exposing the coated sheet to a wet nitrogen atmosphere at 650 to 900°C so as to remove organic components; and

firing the sheet at 850 to 1,050°C after the exposing.